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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: G. Kim)
Applicant's Ref: 59472-8822.US00)
Serial No.: 09/989,647)
Filed: November 20, 2001)
Title: **LOGIC GATES INCLUDING**)
DIODE-CONNECTED METAL-OXIDE-)
SEMICONDUCTOR FIELD-EFFECT)
TRANSISTORS (MOSFETS) TO)
CONTROL INPUT THRESHOLD)
VOLTAGE LEVELS AND SWITCHING)
TRANSIENTS OF OUTPUT LOGIC)
SIGNALS)
_____)

Examiner: J. Joseph
Group Art Unit: 2634
Date: May 23, 2005
Confirmation No.: 1014

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on May 23, 2005.

Signed: Jennifer Mahoney
Jennifer Mahoney

RESTRICTION RESPONSE AND AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement dated April 20, 2005, please enter the following amendments and consider the following remarks.